

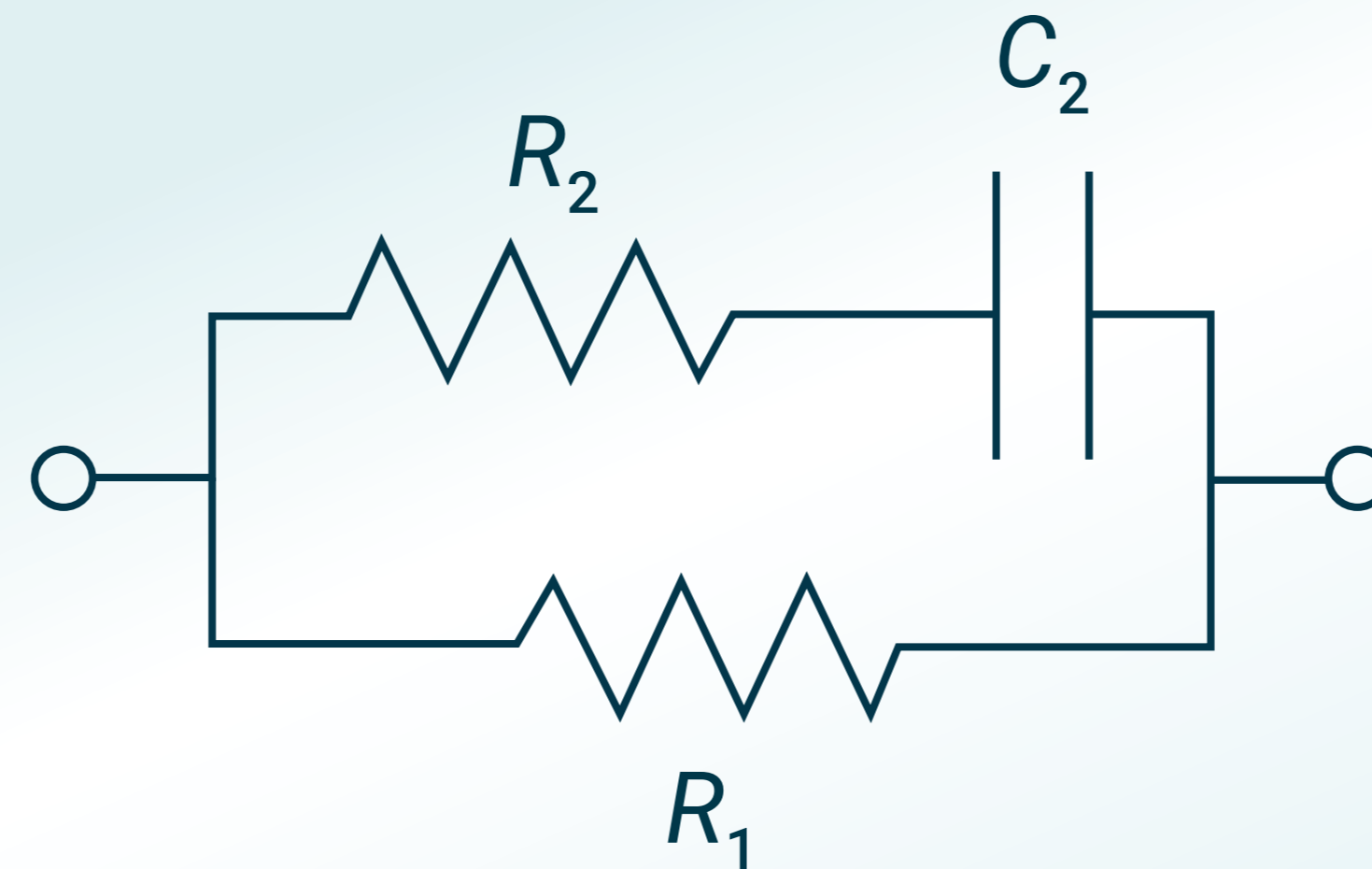
A METHOD FOR PARAMETER ESTIMATION OF THE 2R-1C MODEL SUITABLE FOR LOW-COST EMBEDDED HARDWARE

1. Non-Iterative method without need for initial values.

2. Parameter estimation with a set of closed-form expressions.

3. MCU-based implementation requires 6306 bytes of flash memory and 1396 bytes of RAM.

4. Around 40 times faster than CNLS.



	R_1 [Ω]	R_2 [Ω]	C_2 [nF]
Nominal	$1000 \pm 5\%$	$470 \pm 5\%$	$4.7 \pm 5\%$
Measured	983.37	463.49	4.55
Estimated	972.63	479.62	4.49

Verified with simulations and experimentally obtained data

